

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 01997-242001	Application No. 09/300,641
Information Disclosure Statement by Applicant (Us s veral sheets if necessary) (37 CFR §1.98(b))		Applicant Arvind Mithal et al.	
		Filing Date April 27, 1999	Group Art Unit 2784

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,749,095	05/05/98	Hagersten			
	AB	5,778,423	07/07/98	Sites et al.			
	AC	5,829,025	10/27/98	Mittal			
	AD	5,860,126	01/12/99	Mittal			
	AE	5,873,117	02/16/99	Hagersten et al.			
	AF	5,887,138	03/23/99	Hagersten et al.			
	AG	5,893,144	04/06/99	Wood et al.			
	AH	6,038,642	03/14/00	Arimilli et al.			

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AI	Aditya et al., "Semantics pf pH: A parallel dialect of Haskell", Computation Structures Group Memo, 377-1, June 7, 1995
	AJ	Adve et al., "Weak Ordering - A New Definition", IEEE, 1990
	AK	Adve et al., "A Unified Formalization of Four Shared-Memory Models", Computer Sciences Technical Report #1051, 1992
	AL	Adve et al., "Shared Memory Consistency Models: A Tutorial", IEEE, September 1995
	AM	Adve et al., "A Retrospective on 'Weak Ordering - A New Definition'", First 25 International Symposia on Computer Architecture, ACM Press, 1998
	AN	Ang et al., "The Start-Voyager Parallel System", International Conference on Parallel Architectures and Compilation Techniques (PACT '98), October 13-17, 1998, Paris, France
	AO	Bennett et al., "Adaptive Software Cache Management for Distributed Shared Memory Architectures", Proceedings of the 17 th Annual International Symposium on Computer Architecture, May 1990
	AP	Blumofe et al., "An Analysis of Dag-Consistent Distributed Shared-Memory Algorithms", Proceedings of the Eighth Annual ACM Symposium on Parallel Algorithms and Architectures (SPAA), June 24, 26, 1996, Padua, Italy
	AQ	Chandra et al., "Teapot: Language Support for Writing Memory Coherence Protocols", SIGPLAN Conference on Programming Language Design and Implementation (PLDI), May 1996
	AR	Cox et al., "Adaptive Cache Coherency for Detecting Migratory Shared Data", ISCA93, May 1993
	AS	Eggers et al., "Evaluating the Performance of Four Snooping Cache Coherency Protocols", Proceedings of the 16 th Annual International Symposium on Computer Architecture, May 1989
	AT	Falsafi et al., "Application-Specific Protocols for User-Level Shared Memory", Supercomputing, November 1994
	AU	Frigo et al., "Computation-Centric Memory Models", Proceedings of the 10 th ACM Symposium on Parallel Algorithms and Architectures (SPAA), June 28-July 2, 1998
Examiner Signature		Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		

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Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AV	Gao et al., "Advanced Compilers, Architectures and Parallel Systems", ACAPS Technical Memo 78, December 31, 1993
	AW	Gao et al., "Location Consistency - a New Memory Model and Cache Consistency Protocol", CAPSL Technical Memo 16, February 16, 1998
	AX	Gharachorloo et al., "Memory Consistency and Event Ordering in Scalable Shared-Memory Multiprocessors", The Proceedings of the 17 th International Symposium on Computer Architecture, pages 15-26, May 1990
	AY	Gharachorloo et al., "Two Techniques to Enhance the Performance of Memory Consistency Models", Computer Systems Laboratory, 1991
	AZ	Gharachorloo et al., "Programming for Different Memory Consistency Models", Journal of Parallel and Distributed Computing, 1992
	AAA	Hill, Mark D., "Multiprocessors Should Support Simple Memory Consistency Models", IEEE Computer, 1998
	ABB	Keleher et al., "Lazy Release Consistency for Software Distributed Shared Memory", Department of Computer Science, March 9, 1992
	ACC	Lamport, Leslie, "How to Make a Multiprocessor Computer that Correctly Executes Multiprocess Programs", IEEE Transactions on Computers, Vol. 28, No. 9, September 1979
	ADD	Lenoski et al., "The DASH Prototype: Implementation and Performance", ACM, 1992
	AEE	Mukherjee et al., "Using Protection to Accelerate Coherence Protocols", International Symposium on Computer Architecture, 1998
	AFF	Ranganathan et al., "An Evaluation of Memory Consistency Models for Shared-Memory Systems with ILP Processors", Proceedings of ASPLOS-VII, October 1996
	AGG	Scheurich et al., "Correct Memory Operation of Cache-Based Multiprocessors", ACM, 1987
	AHH	Shen et al., "A Methodology for Designing Correct Cache Coherence Protocols for DSM Systems", Computation Structures Group Memo 398 (A) 1997
	AII	Shen et al., "Modeling and Verification of ISA Implementations", Computation Structures Group Memo 400 (A), June 1997
	AJJ	Stenstrom et al., "An Adaptive Cache Coherence Protocol Optimized for Migratory Sharing", Proceedings of the 20 th Annual International Symposium on Computer Architecture, May 1993
	AKK	Weber et al., "Analysis of Cache Invalidation Patterns in Multiprocessors", Proceedings of the 3 rd International Conference on Architectural Support for Programming Languages and Operating Systems, 1989

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

FORM PTO-892		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. 09/300,641	GROUP ART UNIT 2187	ATTACHMENT TO PAPER NO.	5
NOTICE OF REFERENCES CITED				APPLICANT(S) Mithal et al.			
U.S. PATENT DOCUMENTS							
*		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	A	5,909,697	6/1999	Hayes et al	711	144	
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						
FOREIGN PATENT DOCUMENTS							
*		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB- CLASS
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
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EXAMINER Mehdi Namazi			DATE March 12, 2002		Form892ccs2106b		
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05(a).)							

Notice of References Cited

Application N .

09/300,641

Applicant(s)

Mittal et al.

Examiner

M. NAMAZI

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U.S. PATENT DOCUMENTS

*		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A		6,021,468	Feb, 2000	Arimilli et al.	711	122
B						
C						
D						
E						
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H						
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L						
M						

FOREIGN PATENT DOCUMENTS

*		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
N							
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NON-PATENT DOCUMENTS

*		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
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(See Manual of Patent Examining Procedure, Section 707.05(a).)